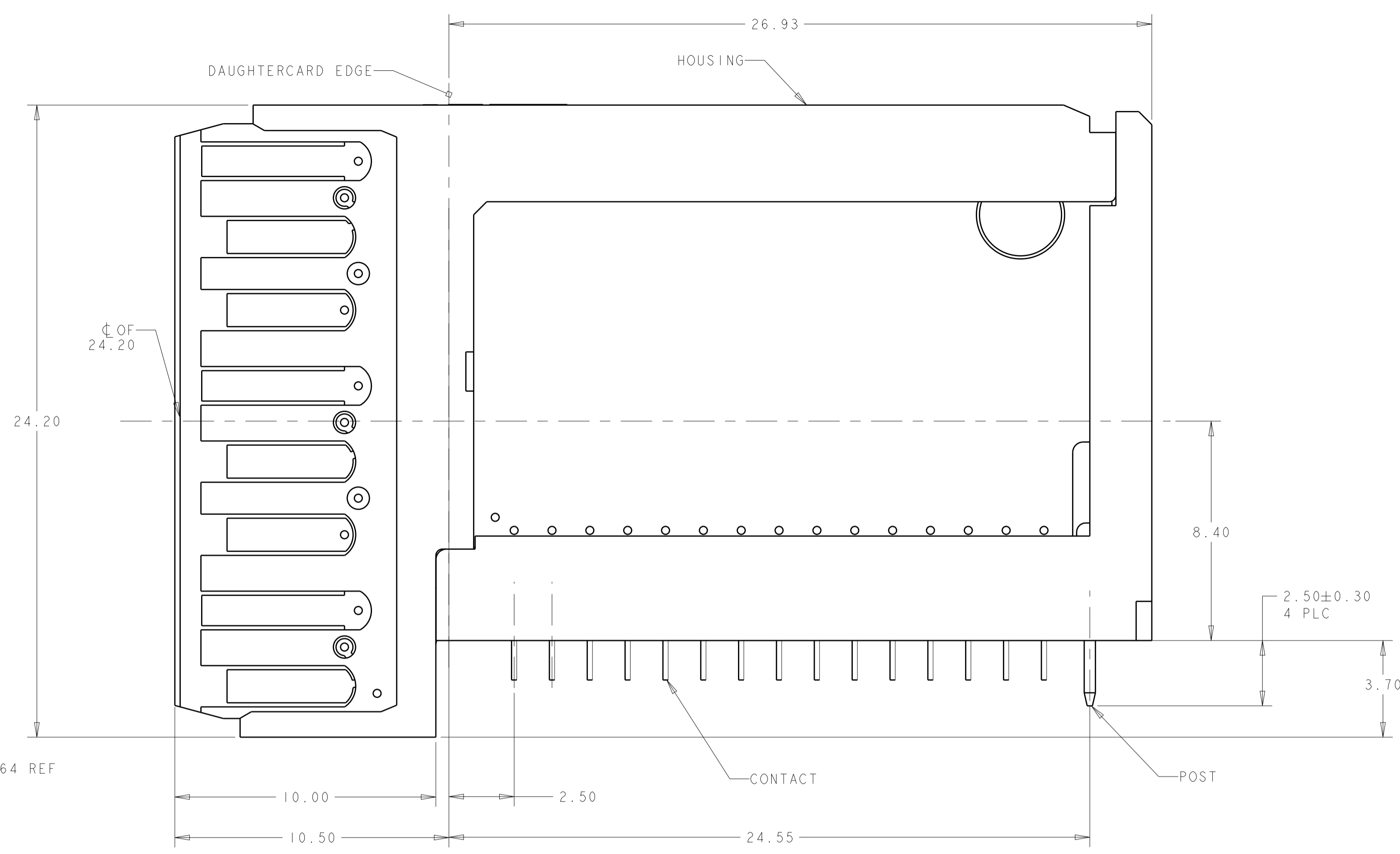
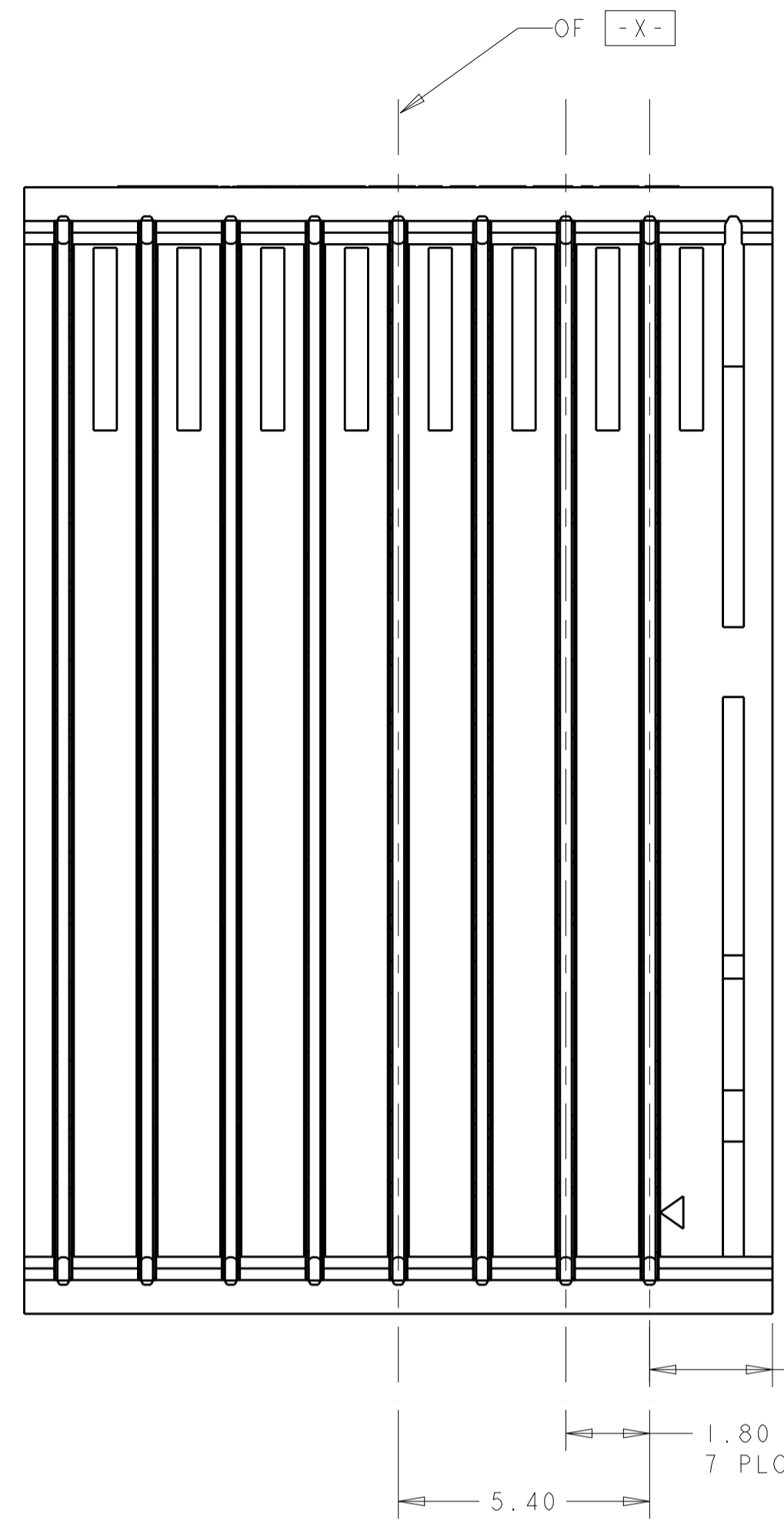
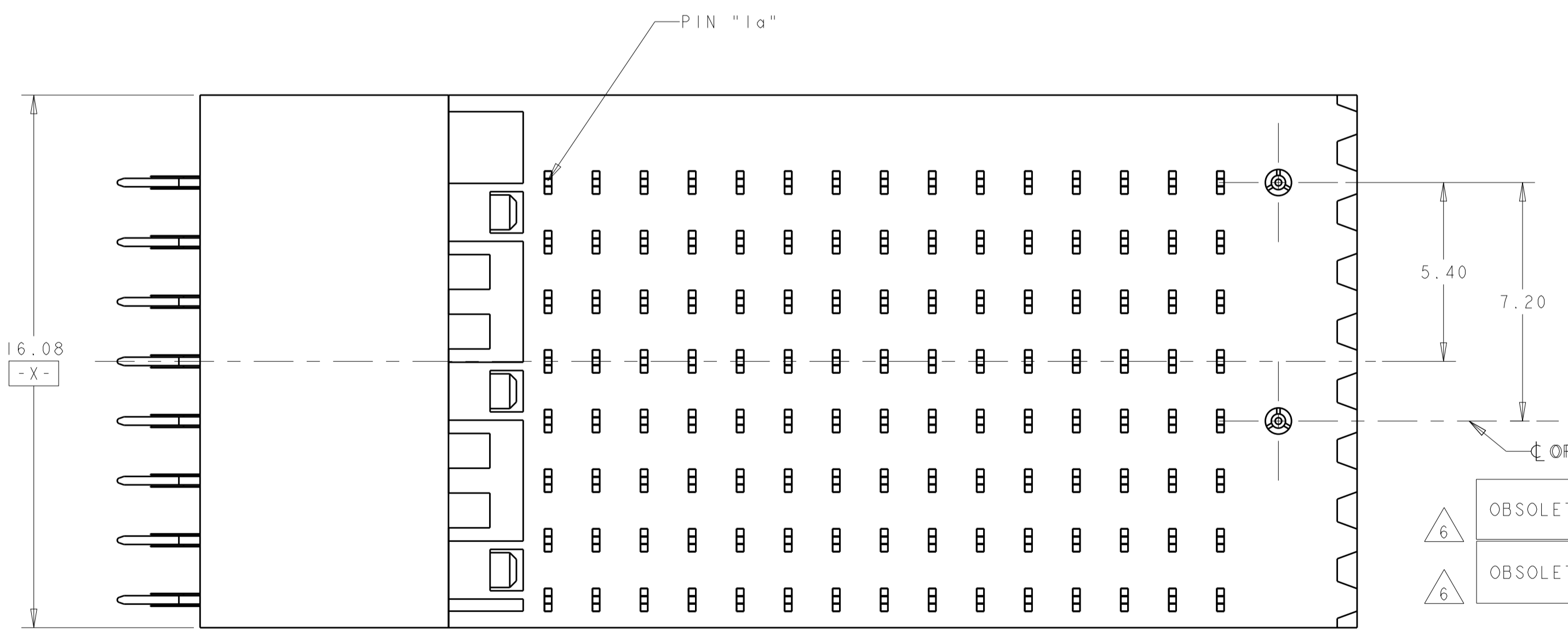
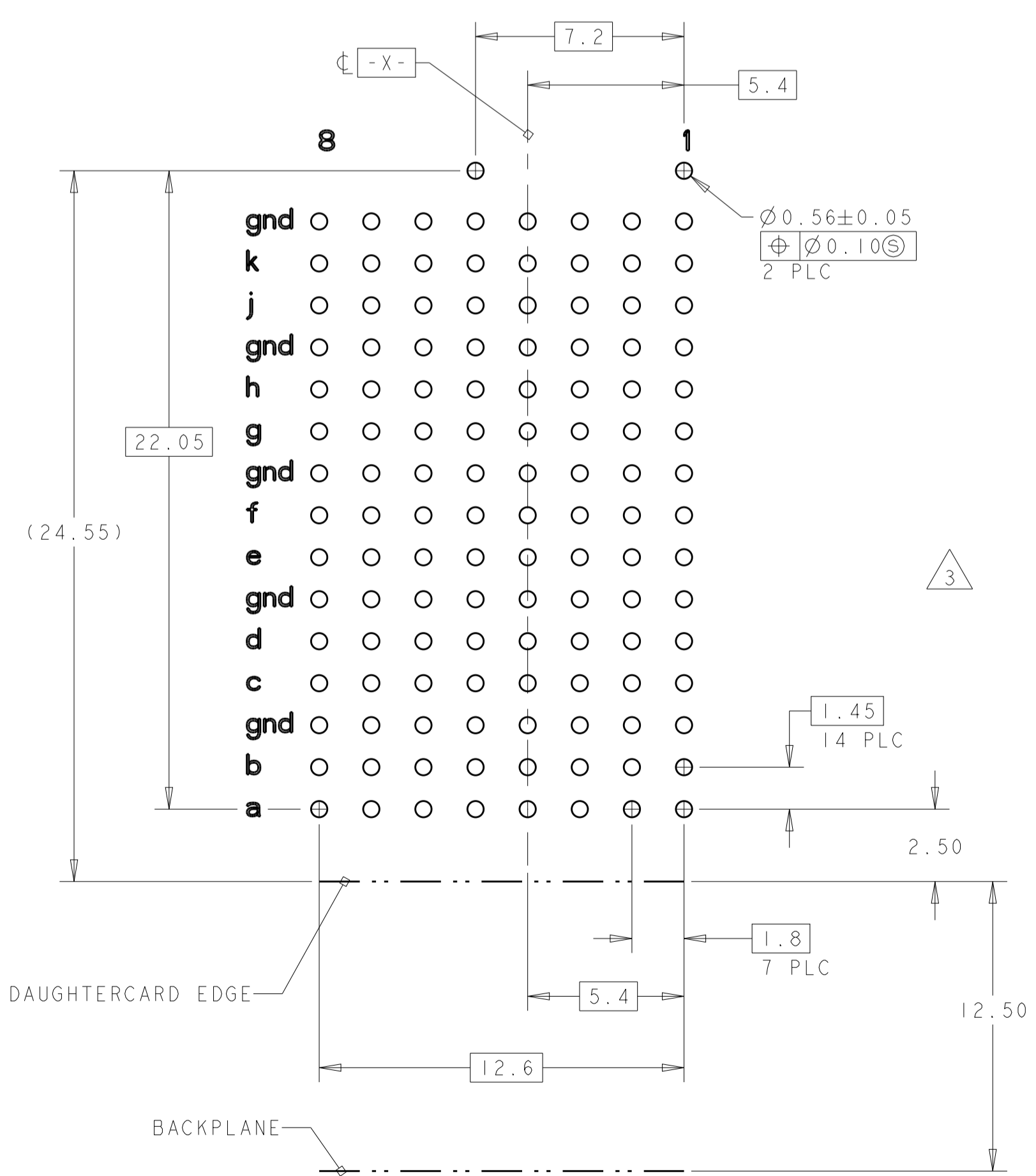


LOC	DIST	REVISIONS					
		P	LTN	DESCRIPTION	DATE	DWN	APVD
AD	00	A2		REVISED PER ECO-12-011861	28JUN2012	KH	AS



- 1 HOUSING: LCP, UL94V-0, BLACK.  
CONTACT: PHOSPHOR BRONZE.  
POST: BRASS WIRE.
- 2 CONTACT: 0.76 μm GOLD PER ASTM B 488 IN CONTACT AREA,  
1.27 μm MIN TIN-LEAD ON PCB TAIL,  
OVER 1.27 μm MIN NICKEL PER SAE AMS-QQ-N-290 OVER ALL.  
POST: 1.27 μm MIN NICKEL.
- 3 PLATED THROUGH HOLE PER 114-13056, FIGURE 4.
- 4 CONTACT: 0.76 μm GOLD PER ASTM B 488 IN CONTACT AREA,  
1.27 μm MIN MATTE TIN PER ASTM B 545 ON PCB TAIL,  
OVER 1.27 μm MIN NICKEL PER SAE AMS-QQ-N-290 OVER ALL.  
POST: 1.27 μm MIN NICKEL.
- 6 OBSOLETE PARTS: OBSOLETE CIS STREAMLINING PER D.RENAUD/D.SINISI



FINISH	DESCRIPTION	PART NUMBER
OBsolete	SINGLE ENDED, NONSEQUENTIAL SIGNAL LEVEL 1, GROUND LEVEL 2	1410232-6
OBsolete	DIFFERENTIAL PAIR, NONSEQUENTIAL SIGNAL LEVEL 2, GROUND LEVEL 3	1410232-5
	SINGLE ENDED, NONSEQUENTIAL SIGNAL LEVEL 2, GROUND LEVEL 3	1410232-4
	SINGLE ENDED, NONSEQUENTIAL SIGNAL LEVEL 1, GROUND LEVEL 2	1410232-3
OBsolete	DIFFERENTIAL PAIR, NONSEQUENTIAL SIGNAL LEVEL 2, GROUND LEVEL 3	1410232-2
	SINGLE ENDED, NONSEQUENTIAL SIGNAL LEVEL 2, GROUND LEVEL 3	1410232-1

PCB LAYOUT (CONNECTOR SIDE) SCALE 6:1

THIS DRAWING IS A CONTROLLED DOCUMENT.

OWN: M.E. CONHEP 28SEP2001  
CHK: D. DEBRAUGH 23OCT2001  
APVD: F.P. MORANA 23OCT2001

TE Connectivity

RIGHT ANGLE, PLUG ASSEMBLY, LEFT END HALF, 25.4mm, MultiGig RT 1, DAUGHTERCARD CONNECTOR

SCALE 8:1 SHEET 1 OF 1 REV A2